## In the Claims:

- 1-52. (Canceled)
- 53. (Original) A digital rights management method, comprising the steps of:
  - (a) storing encrypted digital data at a server;
  - (b) providing an integrated circuit that includes:
    - (i) a processor operative to:
      - (A) request said encrypted digital data from the server and
      - (B) decrypt said encrypted digital data, thereby providing decrypted digital data, and
    - (ii) a player operative to transform said decrypted digital data to analog signals;
  - (c) requesting said encrypted digital data from the server, by said processor;
  - (d) receiving said encrypted digital data, by said processor;
  - (e) storing said received encrypted digital data in a memory separate from said integrated circuit, by said processor;
  - (f) decrypting said received encrypted digital data, by said processor, thereby providing said decrypted digital data; and
  - (g) transforming said decrypted digital data to analog signals, by said player.
- 54. (Original) A digital rights management method, comprising the steps of:

- (a) storing encrypted digital data at a server;
- (b) providing an integrated circuit that includes:
  - (i) a processor operative to:
    - (A) request said encrypted digital data from the server and
    - (B) decrypt said encrypted digital data, using at least one key, thereby providing decrypted digital data, and
  - (ii) a player operative to transform said decrypted digital data to analog signals;
- (c) requesting said encrypted digital data and said at least one key from the server, by said processor;
- (d) storing said at least one key in a nonvolatile memory that is separate from said integrated circuit;
- (e) decrypting said encrypted digital data, by said processor, thereby providing said decrypted digital data; and
- (f) transforming said decrypted digital data to analog signals, by said player.
- 55. (Original) A digital rights management method comprising the steps of:
  - (a) storing encrypted digital data at a server;
  - (b) providing an integrated circuit that includes:
    - (i) a processor operative to:
      - (A) request said encrypted digital data from the server and
      - (B) decrypt said encrypted digital data, thereby providing decrypted digital data, and

- (ii) a player operative to transform said decrypted digital data to analog signals;
- (c) requesting said encrypted digital data from the server, by said processor;
- (d) receiving said encrypted digital data from the server, by said processor;
- (e) decrypting said encrypted digital data, by said processor, thereby providing said decrypted digital data; and
- (f) transforming said decrypted digital data to analog signals, by said player;

wherein said decrypting and said transforming are effected only after all said encrypted digital data have been received from the server.